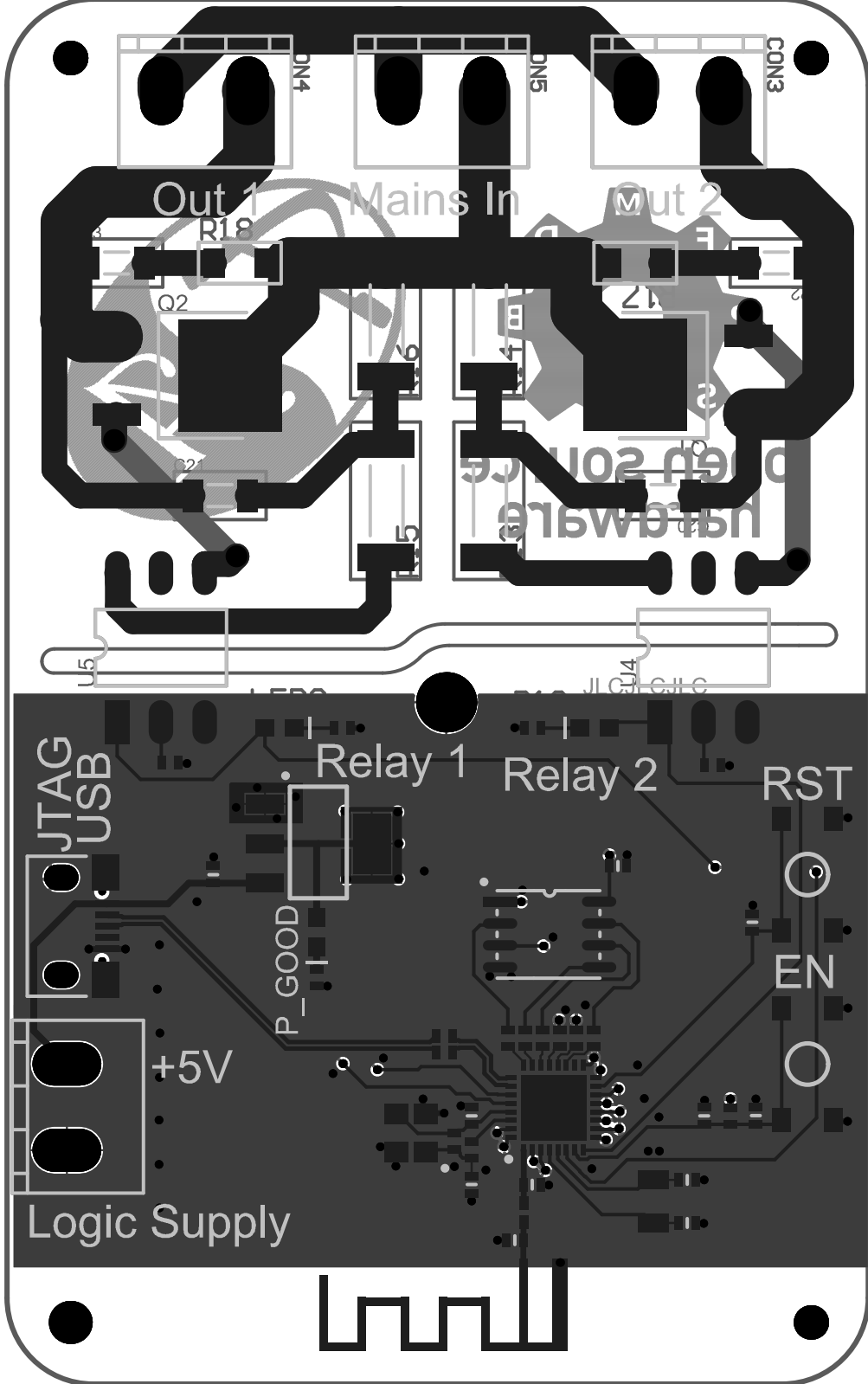


Project:	Date: 2024-08-28
Function:	Revision: 1.0
Size: A3	Designer: Fehér Áron
Sheet: 1	Of 1
File: Controller.SchDoc	





Out 1

Mains In

Out 2

R18

Q2

Q1

R21

R20

R22

R19

U5

U4

JTAG  
USB

Relay 1

Relay 2

RST

P\_GOOD

EN

+5V

Logic Supply

