	before	after
SENS_SAR_READER1_CTRL_REG	0x20040002	0x20040002
SENS_SAR_READER1_STATUS_REG	0x20000000	0x20000000
SENS_SAR_MEAS1_CTRL1_REG	0x00000000	0x00000000
SENS_SAR_MEAS1_CTRL2_REG	0x00000000	0x00000000
SENS_SAR_MEAS1_MUX_REG	0x00000000	0x00000000
SENS_SAR_ATTEN1_REG	0xFFFFFFFF	0xFFFFFFFF
SENS_SAR_AMP_CTRL1_REG	0x000A000A	0x000A000A
SENS_SAR_AMP_CTRL2_REG	0x000A0000	0x000A0000
SENS_SAR_AMP_CTRL3_REG	0x007338F3	0x007338F3
SENS_SAR_READER2_CTRL_REG	0x40050002	0x40050002
SENS_SAR_READER2_STATUS_REG	0x00000000	0x00000000
SENS_SAR_MEAS2_CTRL1_REG	0x07020200	0x07020200
SENS_SAR_MEAS2_CTRL2_REG	0x00000000	0x00000000
SENS_SAR_MEAS2_MUX_REG	0x00000000	0x00000000
SENS_SAR_ATTEN2_REG	0xFFFFFFFF	0xFFFFFFFF
SENS_SAR_POWER_XPD_SAR_REG	0x00000000	0x00000000
SENS_SAR_SLAVE_ADDR1_REG	0x00000000	0x00000000
SENS_SAR_SLAVE_ADDR2_REG	0x00000000	0x00000000
SENS_SAR_SLAVE_ADDR3_REG	0x00000000	0x00000000
SENS_SAR_SLAVE_ADDR4_REG	0x00000000	0x00000000
SENS_SAR_TSENS_CTRL_REG	0x00019000	0x00019000
SENS_SAR_TSENS_CTRL2_REG	0x00004002	0x00004002
SENS_SAR_I2C_CTRL_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_CONF_REG	0xFFF47FFF	0xFFF47FFF
SENS_SAR_TOUCH_DENOISE_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_THRES1_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_THRES2_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_THRES3_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_THRES4_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_THRES5_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_THRES6_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_THRES7_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_THRES8_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_THRES9_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_THRES10_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_THRES11_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_THRES12_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_THRES13_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_THRES14_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_CHN_ST_REG	0x00000000	0x00000000
SENS_SAR_TOUCH_STATUS0_REG	0x03C00000	0x03C00000
SENS_SAR_TOUCH_STATUS1_REG	0xA01CDB6C	0xA01CDB6C
SENS_SAR_TOUCH_STATUS2_REG	0xC01F5EFE	0xC01F5EFE
SENS_SAR_TOUCH_STATUS3_REG	0xE03BF1A9	0xE03BF1A9
SENS_SAR_TOUCH_STATUS4_REG	0xC02F93DF	0xC02F93DF
SENS_SAR_TOUCH_STATUS5_REG	0xA01D3F02	0xA01D3F02
SENS_SAR_TOUCH_STATUS6_REG	0xE007BCB5	0xE007BCB5
SENS_SAR_TOUCH_STATUS7_REG	0xA031FE9D	0xA031FE9D
SENS_SAR_TOUCH_STATUS8_REG	0x20332B73	0x20332B73
SENS_SAR_TOUCH_STATUS9_REG	0x203F29EE	0x203F29EE
SENS_SAR_TOUCH_STATUS10_REG	0xA03A6DB7	0xA03A6DB7
SENS_SAR_TOUCH_STATUS11_REG	0x003BDF2D	0x003BDF2D

SENS_SAR_TOUCH_STATUS12_REG	0xA03CFA8C	0xA03CFA8C
SENS_SAR_TOUCH_STATUS13_REG	0xE01F33F6	0xE01F33F6
SENS_SAR_TOUCH_STATUS14_REG	0xC015EDE9	0xC015EDE9
SENS_SAR_TOUCH_SLP_STATUS_REG	0x003FFFFF	0x003FFFFF
SENS_SAR_TOUCH_APPR_STATUS_REG	0x00000000	0x00000000
SENS_SAR_COCPU_STATE_REG	0x50000000	0x50000000
SENS_SAR_COCPU_INT_RAW_REG	0x00000000	0x00000100
SENS_SAR_COCPU_INT_ENA_REG	0x00000000	0x00000000
SENS_SAR_COCPU_INT_ST_REG	0x00000000	0x00000000
SENS_SAR_COCPU_INT_CLR_REG	0x00000000	0x00000000
SENS_SAR_COCPU_DEBUG_REG	0x00000000	0x00000000
SENS_SAR_HALL_CTRL_REG	0xA0000000	0xA0000000
SENS_SAR_NOUSE_REG	0x00000000	0x00000000
SENS_SAR_PERI_CLK_GATE_CONF_REG	0x40000000	0x40000000
SENS_SAR_PERI_RESET_CONF_REG	0x00000000	0x00000000
SENS_SAR_COCPU_INT_ENA_W1TS_REG	0x00000000	0x00000000
SENS_SAR_COCPU_INT_ENA_W1TC_REG	0x00000000	0x00000000
SENS_SAR_DEBUG_CONF_REG	0x00000000	0x00000000
SENS_SARDATE_REG	0x02101180	0x02101180
RTC_GPIO_OUT_REG	0x00000000	0x00000000
RTC_GPIO_OUT_W1TS_REG	0x00000000	0x00000000
RTC_GPIO_OUT_W1TC_REG	0x00000000	0x00000000
RTC_GPIO_ENABLE_REG	0x00000000	0x00000000
RTC_GPIO_ENABLE_W1TS_REG	0x00000000	0x00000000
RTC_GPIO_ENABLE_W1TC_REG	0x00000000	0x00000000
RTC_GPIO_STATUS_REG	0x00000000	0x00000000
RTC_GPIO_STATUS_W1TS_REG	0x00000000	0x00000000
RTC_GPIO_STATUS_W1TC_REG	0x00000000	0x00000000
RTC_GPIO_IN_REG	0x7B2FCC00	0x11F83C00
RTC_GPIO_PIN0_REG	0x00000000	0x00000000
RTC_GPIO_PIN1_REG	0x00000000	0x00000000
RTC_GPIO_PIN2_REG	0x00000000	0x00000000
RTC_GPIO_PIN3_REG	0x00000000	0x00000000
RTC_GPIO_PIN4_REG	0x00000000	0x00000000
RTC_GPIO_PIN5_REG	0x00000000	0x00000000
RTC_GPIO_PIN6_REG	0x00000000	0x00000000
RTC_GPIO_PIN7_REG	0x00000000	0x00000000
RTC_GPIO_PIN8_REG	0x00000000	0x00000000
RTC_GPIO_PIN9_REG	0x00000000	0x00000000
RTC_GPIO_PIN10_REG	0x00000000	0x00000000
RTC_GPIO_PIN11_REG	0x00000000	0x00000000
RTC_GPIO_PIN12_REG	0x00000000	0x00000000
RTC_GPIO_PIN13_REG	0x00000000	0x00000000
RTC_GPIO_PIN14_REG	0x00000000	0x00000000
RTC_GPIO_PIN15_REG	0x00000000	0x00000000
RTC_GPIO_PIN16_REG	0x00000000	0x00000000
RTC_GPIO_PIN17_REG	0x00000000	0x00000000
RTC_GPIO_PIN18_REG	0x00000000	0x00000000
RTC_GPIO_PIN19_REG	0x00000000	0x00000000
RTC_GPIO_PIN20_REG	0x00000000	0x00000000
RTC_GPIO_PIN21_REG	0x00000000	0x00000000
RTC_IO_RTC_DEBUG_SEL_REG	0x00000000	0x00000000
RTC_IO_TOUCH_PAD0_REG	0x50000000	0x50000000

RTC_IO_TOUCH_PAD1_REG	0x48000000	0x48000000
RTC_IO_TOUCH_PAD2_REG	0x50000000	0x50000000
RTC_IO_TOUCH_PAD3_REG	0x48000000	0x48000000
RTC_IO_TOUCH_PAD4_REG	0x50000000	0x50000000
RTC_IO_TOUCH_PAD5_REG	0x50000000	0x50000000
RTC_IO_TOUCH_PAD6_REG	0x48000000	0x48000000
RTC_IO_TOUCH_PAD7_REG	0x40000000	0x40000000
RTC_IO_TOUCH_PAD8_REG	0x40000000	0x40000000
RTC_IO_TOUCH_PAD9_REG	0x40000000	0x40000000
RTC_IO_TOUCH_PAD10_REG	0x40000000	0x40000000
RTC_IO_TOUCH_PAD11_REG	0x40000000	0x40000000
RTC_IO_TOUCH_PAD12_REG	0x40000000	0x40000000
RTC_IO_TOUCH_PAD13_REG	0x40000000	0x40000000
RTC_IO_TOUCH_PAD14_REG	0x40000000	0x40000000
RTC_IO_XTAL_32P_PAD_REG	0x40000000	0x40000000
RTC_IO_XTAL_32N_PAD_REG	0x40000000	0x40000000
RTC_IO_PAD_DAC1_REG	0x40000000	0x40000000
RTC_IO_PAD_DAC2_REG	0x40000000	0x40000000
RTC_IO_RTC_PAD19_REG	0x50000000	0x50000000
RTC_IO_RTC_PAD20_REG	0x50000000	0x50000000
RTC_IO_RTC_PAD21_REG	0x50000000	0x50000000
RTC_IO_EXT_WAKEUP0_REG	0x00000000	0x00000000
RTC_IO_XTL_EXT_CTR_REG	0x00000000	0x00000000
RTC_IO_SAR_I2C_IO_REG	0x00000000	0x00000000
RTC_IO_TOUCH_CTRL_REG	0x00000000	0x00000000
RTC_IO_DATE_REG	0x02101180	0x02101180
RTC_CNTL_TIME0_REG	0x0000AD04	0x0000AD04
RTC_CNTL_TIME1_REG	0x00000000	0x00000000
RTC_CNTL_OPTIONS0_REG	0x1C008000	0x1C008000
RTC_CNTL_SLP_TIMER0_REG	0x00000000	0x00000000
RTC_CNTL_SLP_TIMER1_REG	0x00000000	0x00000000
RTC_CNTL_TIME_UPDATE_REG	0x00000000	0x00000000
RTC_CNTL_TIME_LOW0_REG	0x0000AD04	0x0000AD04
RTC_CNTL_TIME_HIGH0_REG	0x00000000	0x00000000
RTC_CNTL_STATE0_REG	0x20000000	0x20000000
RTC_CNTL_TIMER1_REG	0x14140143	0x14140143
RTC_CNTL_TIMER2_REG	0x01080000	0x01080000
RTC_CNTL_TIMER3_REG	0x02010201	0x02010201
RTC_CNTL_TIMER4_REG	0x02010201	0x02010201
RTC_CNTL_TIMER5_REG	0x00000200	0x00000200
RTC_CNTL_TIMER6_REG	0x02010201	0x02010201
RTC_CNTL_ANA_CONF_REG	0x00400000	0x00400000
RTC_CNTL_RESET_STATE_REG	0x0000F041	0x0000F041
RTC_CNTL_WAKEUP_STATE_REG	0x00060000	0x00060000
RTC_CNTL_INT_ENA_REG	0x00000200	0x00000200
RTC_CNTL_INT_RAW_REG	0x00000000	0x00008000
RTC_CNTL_INT_ST_REG	0x00000000	0x00000000
RTC_CNTL_INT_CLR_REG	0x00000000	0x00000000
RTC_CNTL_STORE0_REG	0x00000000	0x00000000
RTC_CNTL_STORE1_REG	0x0038C49A	0x0038C49A
RTC_CNTL_STORE2_REG	0x00000000	0x00000000
RTC_CNTL_STORE3_REG	0x00000000	0x00000000
RTC_CNTL_EXT_XTL_CONF_REG	0x00066C80	0x00066C80

RTC_CNTL_EXT_WAKEUP_CONF_REG	0x00000000	0x00000000
RTC_CNTL_SLP_REJECT_CONF_REG	0x00000000	0x00000000
RTC_CNTL_CPU_PERIOD_CONF_REG	0x00000000	0x00000000
RTC_CNTL_SDIO_ACT_CONF_REG	0x00000000	0x00000000
RTC_CNTL_CLK_CONF_REG	0x30C8069C	0x30C8069C
RTC_CNTL_SLOW_CLK_CONF_REG	0x00400000	0x00400000
RTC_CNTL_SDIO_CONF_REG	0x0AB0BE0A	0x0AB0BE0A
RTC_CNTL_BIAS_CONF_REG	0x00010800	0x00010800
RTC_CNTL_REG	0x103FC000	0x103FC000
RTC_CNTL_PWC_REG	0x00000000	0x00000000
RTC_CNTL_REGULATOR_DRV_CTRL_REG	0x00000000	0x00000000
RTC_CNTL_DIG_PWC_REG	0x00020000	0x00020000
RTC_CNTL_DIG_ISO_REG	0x10000080	0x10000080
RTC_CNTL_WDTCONFIG0_REG	0x4007EE00	0x4007EE00
RTC_CNTL_WDTCONFIG1_REG	0x000956A0	0x000956A0
RTC_CNTL_WDTCONFIG2_REG	0x00013880	0x00013880
RTC_CNTL_WDTCONFIG3_REG	0x00000FFF	0x00000FFF
RTC_CNTL_WDTCONFIG4_REG	0x00000FFF	0x00000FFF
RTC_CNTL_WDTFEED_REG	0x00000000	0x00000000
RTC_CNTL_WDTWPROTECT_REG	0x00000000	0x00000000
RTC_CNTL_SWD_CONF_REG	0x84B00000	0x84B00000
RTC_CNTL_SWD_WPROTECT_REG	0x00000000	0x00000000
RTC_CNTL_SW_CPU_STALL_REG	0x00000000	0x00000000
RTC_CNTL_STORE4_REG	0x00280028	0x00280028
RTC_CNTL_STORE5_REG	0x00000000	0x00000000
RTC_CNTL_STORE6_REG	0x00000000	0x00000000
RTC_CNTL_STORE7_REG	0x00000000	0x00000000
RTC_CNTL_LOW_POWER_ST_REG	0x08150130	0x08150130
RTC_CNTL_DIAG0_REG	0x02719000	0x02719000
RTC_CNTL_PAD_HOLD_REG	0x00000000	0x00000000
RTC_CNTL_DIG_PAD_HOLD_REG	0x00000000	0x00000000
RTC_CNTL_EXT_WAKEUP1_REG	0x00000000	0x00000000
RTC_CNTL_EXT_WAKEUP1_STATUS_REG	0x00000000	0x00000000
RTC_CNTL_BROWN_OUT_REG	0x4BFFC020	0x4BFFC020
RTC_CNTL_TIME_LOW1_REG	0x0000AA35	0x0000AA35
RTC_CNTL_TIME_HIGH1_REG	0x00000000	0x00000000
RTC_CNTL_XTAL32K_CLK_FACTOR_REG	0x00000000	0x00000000
RTC_CNTL_XTAL32K_CONF_REG	0x0FF00000	0x0FF00000
RTC_CNTL_ULP_CP_TIMER_REG	0x00000000	0x00000000
RTC_CNTL_ULP_CP_CTRL_REG	0x00100200	0x00100200
RTC_CNTL_COCPU_CTRL_REG	0x008A0810	0x008A0810
RTC_CNTL_TOUCH_CTRL1_REG	0x10000100	0x10000100
RTC_CNTL_TOUCH_CTRL2_REG	0x000840CC	0x000840CC
RTC_CNTL_TOUCH_SCAN_CTRL_REG	0xF0000102	0xF0000102
RTC_CNTL_TOUCH_SLP_THRES_REG	0x78000000	0x78000000
RTC_CNTL_TOUCH_APPROACH_REG	0x50000000	0x50000000
RTC_CNTL_TOUCH_FILTER_CTRL_REG	0x96AA8800	0x96AA8800
RTC_CNTL_USB_CONF_REG	0x00000000	0x00000000
RTC_CNTL_TOUCH_TIMEOUT_CTRL_REG	0x007FFFFF	0x007FFFFF
RTC_CNTL_SLP_REJECT_CAUSE_REG	0x00000000	0x00000000
RTC_CNTL_OPTION1_REG	0x00000000	0x00000000
RTC_CNTL_SLP_WAKEUP_CAUSE_REG	0x00000000	0x00000000
RTC_CNTL_ULP_CP_TIMER_1_REG	0x0000C800	0x0000C800

RTC_CNTL_INT_ENA_W1TS_REG	0x00000000	0x00000000
RTC_CNTL_INT_ENA_W1TC_REG	0x00000000	0x00000000
RTC_CNTL_RETENTION_CTRL_REG	0x28340000	0x28340000
RTC_CNTL_PG_CTRL_REG	0x00000000	0x00000000
RTC_CNTL_FIB_SEL_REG	0x00000000	0x00000000
RTC_CNTL_TOUCH_DAC_REG	0x00000000	0x00000000
RTC_CNTL_TOUCH_DAC1_REG	0x00000000	0x00000000
RTC_CNTL_COCPU_DISABLE_REG	0x00000000	0x00000000
RTC_CNTL_DATE_REG	0x02103271	0x02103271