

I (24) boot: ESP-IDF v5.5.1 2nd stage bootloader
I (24) boot: compile time Oct 15 2025 15:15:56
I (24) boot: chip revision: v0.4
I (24) boot: efuse block revision: v1.3
I (27) boot.esp32c3: SPI Speed : 80MHz
I (31) boot.esp32c3: SPI Mode : DIO
I (35) boot.esp32c3: SPI Flash Size : 2MB
I (39) boot: Enabling RNG early entropy source...
I (43) boot: Partition Table:
I (46) boot: ## Label Usage Type ST Offset Length
I (52) boot: 0 nvs WiFi data 01 02 00009000 00006000
I (59) boot: 1 phy_init RF data 01 01 0000f000 00001000
I (65) boot: 2 factory factory app 00 00 00010000 00177000
I (72) boot: End of partition table
I (75) esp_image: segment 0: paddr=00010020 vaddr=3c0a0020 size=2192ch
(137516) map
I (104) esp_image: segment 1: paddr=00031954 vaddr=3fc93a00 size=0216ch
(8556) load
I (106) esp_image: segment 2: paddr=00033ac8 vaddr=40380000
size=0c550h (50512) load
I (118) esp_image: segment 3: paddr=00040020 vaddr=42000020
size=99138h (627000) map
I (218) esp_image: segment 4: paddr=000d9160 vaddr=4038c550
size=07414h (29716) load
I (224) esp_image: segment 5: paddr=000e057c vaddr=50000000
size=00020h (32) load
I (230) boot: Loaded app from partition at offset 0x10000
I (230) boot: Disabling RNG early entropy source...
I (243) cpu_start: Unicore app
I (251) cpu_start: Pro cpu start user code
I (251) cpu_start: cpu freq: 160000000 Hz
I (252) app_init: Application information:
I (252) app_init: Project name: esp_hid_device
I (256) app_init: App version: v5.5.1
I (260) app_init: Compile time: Oct 15 2025 15:15:42
I (265) app_init: ELF file SHA256: 411659ca0...
--- Warning: Checksum mismatch between flashed and built applications.
Checksum of built application is
4587dd6a03066a7b9d00dc0611b98bf00ce57ce54fdd874c56f94928e887c3e
3
I (269) app_init: ESP-IDF: v5.5.1
I (273) efuse_init: Min chip rev: v0.3
I (277) efuse_init: Max chip rev: v1.99
I (281) efuse_init: Chip rev: v0.4
I (285) heap_init: Initializing. RAM available for dynamic allocation:
I (291) heap_init: At 3FC99FF0 len 00026010 (152 KiB): RAM
I (296) heap_init: At 3FCC0000 len 0001C710 (113 KiB): Retention RAM

I (302) heap_init: At 3FCDC710 len 00002950 (10 KiB): Retention RAM
I (308) heap_init: At 50000020 len 00001FC8 (7 KiB): RTCRAM
I (314) spi_flash: detected chip: generic
I (317) spi_flash: flash io: dio W (320) spi_flash: Detected size(4096k) larger than the size in the binary image header(2048k). Using the size in the binary image header.
I (333) sleep_gpio: Configure to isolate all GPIO pins in sleep state
I (339) sleep_gpio: Enable automatic switching of GPIO sleep configuration
I (345) coexist: coex firmware version: b0bcc39
I (350) coexist: coexist rom version 9387209
I (354) main_task: Started on CPU0
I (354) main_task: Calling app_main()
I (354) HID_DEV_DEMO: setting hid gap, mode:1
I (364) BLE_INIT: BT controller compile version [2edb0b0]
I (364) BLE_INIT: Using main XTAL as clock source
I (374) BLE_INIT: Feature Config, ADV:1, BLE_50:1, DTM:1, SCAN:1, CCA:0, SMP:1, CONNECT:1
I (374) BLE_INIT: Bluetooth MAC: 50:78:7d:43:04:ca
I (384) phy_init: phy_version 1201,bae5dd99,Mar 3 2025,15:36:21
I (414) phy_init: Saving new calibration data due to checksum failure or outdated calibration data, mode(0)
W (444) BT_BTMM: BTM_BleWriteAdvData, Partial data write into ADV
I (444) HID_DEV_DEMO: setting ble device
I (454) HID_DEV_BLE: START
I (454) main_task: Returned from app_main()